

<b>Notice of Allowability</b>	Application No.	Applicant(s)	
	10/764,009	VAN BROCKLIN ET AL.	
	Examiner	Art Unit	
	Jennifer M. Dolan	2813	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1.  This communication is responsive to Pre-Amendt. 1/23/04.
2.  The allowed claim(s) is/are 35-51.
3.  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a)  All
  - b)  Some\*
  - c)  None
  1.  Certified copies of the priority documents have been received.
  2.  Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3.  Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4.  A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5.  CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
  - (a)  including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
    - 1)  hereto or 2)  to Paper No./Mail Date \_\_\_\_\_.
  - (b)  including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6.  DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

1.  Notice of References Cited (PTO-892)
2.  Notice of Draftsperson's Patent Drawing Review (PTO-948)
3.  Information Disclosure Statements (PTO-1449 or PTO/SB/08),  
Paper No./Mail Date 1/23/04;6/27/05
4.  Examiner's Comment Regarding Requirement for Deposit  
of Biological Material
5.  Notice of Informal Patent Application (PTO-152)
6.  Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_.
7.  Examiner's Amendment/Comment
8.  Examiner's Statement of Reasons for Allowance
9.  Other \_\_\_\_\_.

  
**CARL WHITEHEAD, JR.**  
**SUPERVISORY PATENT EXAMINER**  
**TECHNOLOGY CENTER 2800**

**DETAILED ACTION**

***Allowable Subject Matter***

1. Claims 35-51 are allowed.

2. The following is an examiner's statement of reasons for allowance:

The primary reason for allowance is that the prior art does not fairly indicate a combination of both an electrically linear resistive element and a voltage breakdown element at the intersections between column and row conductors, nor does the prior art teach forming the voltage breakdown element as an unpatterned element.

Generally, the prior art of record teaches cells formed at the intersection of row and column conductors including a diode structure and an antifuse/voltage breakdown element in a series, such as US 5,818,749 to Harshfield or US 2001/0055838 to Walker et al. (cited by applicant). It is apparent, however, that a linear resistor and a diode have vastly different current-voltage properties, such that it would be unreasonable to arbitrarily substitute one for the other. Alternately, US 5,714,416 to Eichman et al. does teach a resistance element in series with a voltage breakdown element, but the resistance element merely indicates internal resistances of the voltage breakdown structure, and hence, cannot be considered linear nor can it be considered a separate element, as opposed to double inclusion of the voltage breakdown structure.

Furthermore, the prior art of record generally teaches patterning all layers disposed at the intersection of row and column conductors, such that a cell column is formed, and such that current leakage between adjacent memory cells is prevented. However, as the use of a resistor within the memory cell minimizes current leakage through cells that are not to be accessed, the

presence of the resistor enables employment of an unpatterned breakdown element, which is cheaper and easier to fabricate. Hence, it is the Examiner's opinion that a person skilled in the art would not have found sufficient motivation in the prior art to combine the linear resistor and the voltage breakdown element in series between a row and column conductor, and further, would have found no motivation or feasible means for using an unpatterned voltage breakdown element.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jennifer M. Dolan whose telephone number is (571) 272-1690. The examiner can normally be reached on Monday-Friday 8:30am-5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Carl W. Whitehead, Jr. can be reached on (571) 272-1702. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Jennifer M. Dolan  
Examiner  
Art Unit 2813

jmd